



See Convey @ SC10 Booth 3947

Convey Computer Adds Five New Partners to Provide Customers with More Performance, Functionality, and Choice for Hybrid-core Systems

New partnerships extend network storage options, personality development, and speech recognition solutions

RICHARDSON, Texas (November 9, 2010) – Convey Computer™ Corporation announced today five new technology and business partnerships that extend application availability for its hybrid-core computing platforms. The solutions provided by these partners help Convey deliver even greater performance, functionality, and choice to their customers.

Technologies such as network-attached storage, personality development tools, and speech recognition solutions are now available for Convey's hybrid-core platforms. The new Convey partners include Panasas, AutoESL, Impulse, Jacquard Computing, and Voci Technologies.

“We are excited about our latest partnerships with such outstanding technology leaders,” said Bruce Toal, CEO, Convey Computer. “By expanding our portfolio of partners, we provide our customers and their end users with more choice and functionality, particularly when it comes to developing personalities for hybrid-core platforms. These partnerships, by such a diverse group of innovators, underscore the acceptance of hybrid-core technology as a successful approach to application acceleration in a variety of industries.”

Convey Adds Five New Partners/

The new Convey partners and their areas of specialization include the following:

- **Network-Attached Storage: Panasas, Inc.**

Panasas is the leader in high performance parallel storage for business critical applications. The company's high performance parallel storage solutions complement Convey hybrid-core systems by eliminating bottlenecks and exploiting parallel I/O and the compute speed of the Convey hybrid-core architecture.

"High performance computing systems, such as those engineered by Convey, need ready and accurate access to extreme amounts of data to satisfy the requirements of today's applications," said Brent Welch, director of architecture at Panasas. "Panasas parallel storage systems are an ideal match for Convey's compute performance." The Panasas client software is available on the ConveyOS and the Convey Cluster Framework—a clustering capability that seamlessly fits Convey hybrid-core computing into existing Linux cluster environments.

- **Personality Development Tools:**

- AutoESL – AutoPilot™
- Impulse – Impulse C™
- Jacquard Computing Inc. – ROCCC

To help customers rapidly develop customized instruction sets (personalities) and innovate on the hybrid-core platforms, Convey has aligned with three partners specializing in high-level FPGA synthesis tools.

AutoESL has developed AutoPilot™, a high-level synthesis solution that enables customers to take complex algorithms in C and C++ and quickly target ASICs and FPGAs without changing the source code. "Leading software and semiconductor companies that have adopted AutoPilot have realized QoR

Convey Adds Five New Partners/

(Quality of Results) that surpasses the quality of hand-coded designs. Additionally, customers see productivity improvements of 75 percent and speedups of 1800x,” said Atul Sharan, president and CEO of AutoESL.

Impulse makes the Impulse C-to-FPGA optimizing compiler for hybrid-core image, signal, and data processing. The company’s software-to-hardware compiler allows HPC developers to create accelerated applications for the Convey hybrid-core architecture without the need to write low level hardware code. “Convey is certainly expanding what is possible. The company’s well-integrated logic configuration, mixing microprocessor and FPGA, gives software/hardware co-developers a unique platform to optimize logic for specific processes. We believe it can have a significant impact on high-throughput and massive data processing and are proud to be part of the tool flow,” said Brian Durwood, co-founder and CEO of Impulse.

Jacquard Computing provides technical services in the development of novel accelerated applications and porting of Riverside Optimizing Compiler for Configurable Computing (ROCCC) 2.0 to FPGA platforms. Walid Najjar, founder and senior consultant at JCI, explained that the “Convey systems are the perfect platform for an automated C-to-Gates synthesis tool like ROCCC. We took a well-known face detection algorithm, Viola-Jones—it’s 17,000 lines of C code—and compiled it using ROCCC. In less than three days, we had the compiler generating VHDL for the Convey system. That’s an implementation record for this kind of development.”

- **Speech Recognition Solutions: Voci Technologies Incorporated**

Voci™ offers the first hardware-accelerated speech recognizer for enterprise analytics applications. The company’s V-Blaze™ appliance—based on Convey hybrid-core technology—operates at speeds orders of magnitude faster than any alternative. Conversations from one hundred phone lines or more can be simultaneously converted into text on a single Voci™ appliance.

Convey Adds Five New Partners/

“By employing hybrid-core computing, our proprietary acceleration technology makes large-scale speech-to-text affordable, more accurate, and highly scalable,” said Anthony Gadiant, Voci CEO. “Enterprise analytics providers can serve their customers with broader speech coverage, now that speech recognition is liberated from the costs and constraints of software-only solutions.”

###

About Convey Computer Corporation

Based in Richardson, Texas, Convey Computer breaks power, performance and programmability barriers with the world’s first hybrid-core computer—a system that marries the low cost and simple programming model of a commodity system with the performance of a customized hardware architecture. Convey brings decades of experience and intellectual assets to performance problem solving. Its executive and design teams all come from successful backgrounds of building computer companies, most notably Convex Computer Corporation and Hewlett-Packard. Convey Computer investors include Braemar Energy Ventures, CenterPoint Ventures, Intel Capital, InterWest Partners, Rho Ventures, and Xilinx. More information can be found at: www.conveycomputer.com.

About AutoESL Design Technologies

AutoESL delivers the industry’s leading high-level synthesis (HLS) solution targeting ASICs and FPGAs. AutoESL provides an easy to adopt and easy to deploy HLS solution offering the industry’s only unified language support of C, C++ and System C for system designers for both ASIC and FPGA designs. AutoESL produces results that are optimized for area, performance and power, resulting in the highest-quality implementation-aware RTL. AutoESL’s products address various markets including DSP, Video, Networking, Wireless and High-Performance Computing. AutoESL is headquartered in Cupertino, CA with offices in China, Europe, Los Angeles and Texas. For more information, visit www.autoesl.com.

About Impulse

Founded in 2002 by members of the original ABEL® team, Impulse Accelerated Technologies makes the Impulse C™-to-FPGA optimizing compiler for hybrid-core image, signal and data processing. Impulse C is a software-to-hardware compiler that allows HPC developers to create accelerated applications for the Convey hybrid-core architecture, without the need to write low level hardware code. Impulse C includes automatic and user-directed pipelining, parallelizing and optimizing of C code for increased performance and reduced power consumption. Impulse also provides IP libraries and services for faster deployment. Impulse C is the most widely used C-to-FPGA optimizing compiler, with hundreds of design wins including most of the automotive companies, dozens of imaging teams, eight of the top ten government contractors, most United States government agencies and hundreds of R&D labs worldwide. For more information, visit www.ImpulseC.com.

About Jacquard Computing, Inc.

With more than 12 years of experience in the compilation of FPGA-based hardware accelerators, JCI provides technical services in the development of novel accelerated applications and porting of ROCCC 2.0 to FPGA platforms. ROCCC 2.0's free and powerful tool set is the tool to use for C-to-VHDL solutions. For more information, visit www.jacquardcomputing.com.

About Panasas

Panasas, Inc., the leader in high-performance scale-out NAS storage solutions, enables enterprise customers to rapidly solve complex computing problems, speed innovation and bring new products to market faster. All Panasas solutions leverage the patented PanFS™ storage operating system to deliver exceptional performance, scalability and manageability. Panasas systems are optimized for demanding storage environments in the energy, government, finance,

Convey Adds Five New Partners/

manufacturing, bioscience and higher education industries. For more information, visit www.panasas.com.

About Voci Technologies

Voci™ delivers high performance speech-to-text appliances for enterprise analytics applications. The company pioneered the implementation of speech recognition algorithms in hardware to achieve the most comprehensive and economical solution for analytics providers, empowering them to derive actionable insights from telephone interactions with customers. On a single appliance, Voci customers can convert between 200,000 to 3,000,000 hours of speech per year. For more information, visit www.vocitec.com.

Convey Computer, the Convey logo, and Convey HC-1 and HC-1^{ex} are trademarks of Convey Computer Corporation in the U.S. and other countries. Intel® and Intel® Xeon® are registered trademarks of Intel Corporation in the U.S. and other countries.

All other trademarks and registered trademarks belong to the respective companies.

For More Information:

At Convey, contact Mary Dudley at 505-989-1477 or email mdudley@conveycomputer.com

At SC10, please visit Convey in Booth 3947 or contact Bob Masson, Convey's director of product marketing, at 720-352-5157 (mobile) or email bmasson@conveycomputer.com